

Fig. 1A

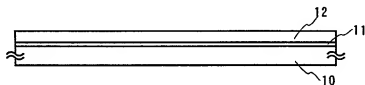


Fig. 1B

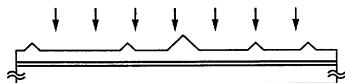


Fig. 1C

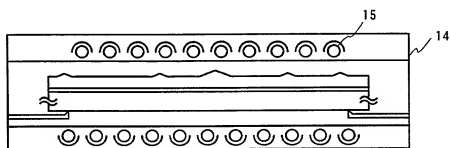


Fig. 2A

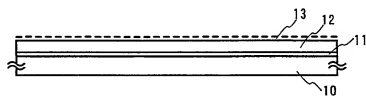


Fig. 2B

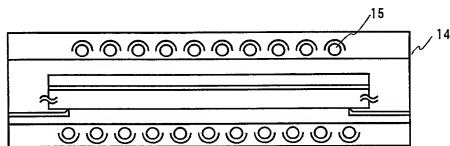


Fig. 2C

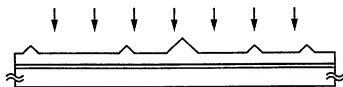
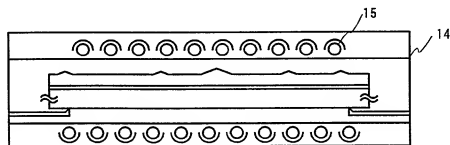


Fig. 2D



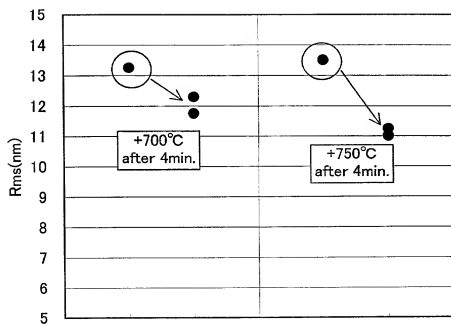


Fig. 3A

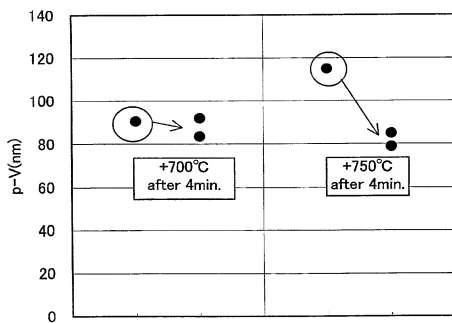
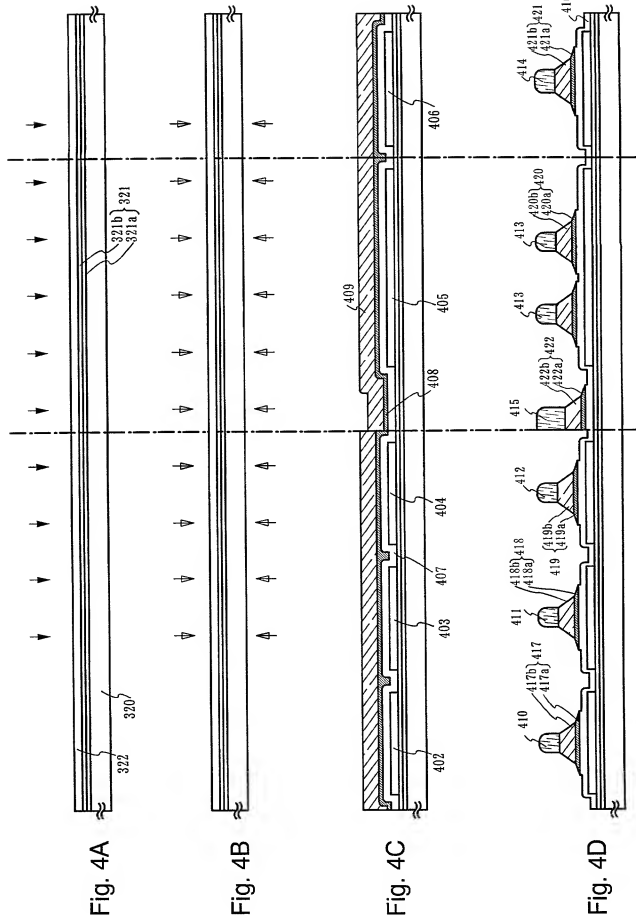


Fig. 3B



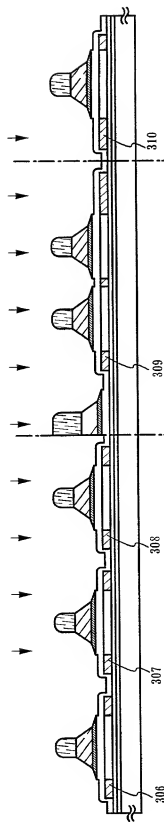


Fig. 5A

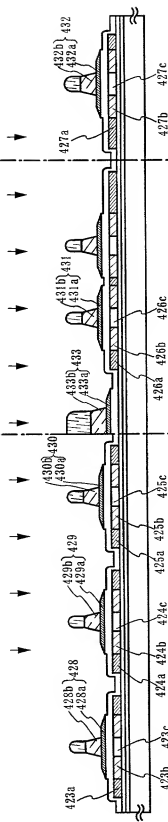


Fig. 5B

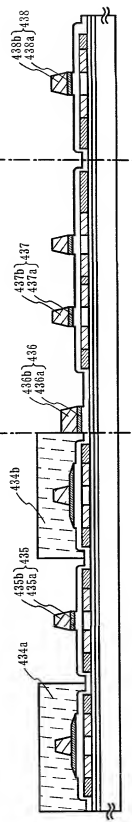


Fig. 5C

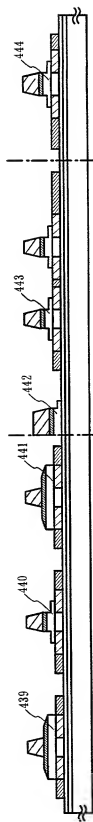


Fig. 6A

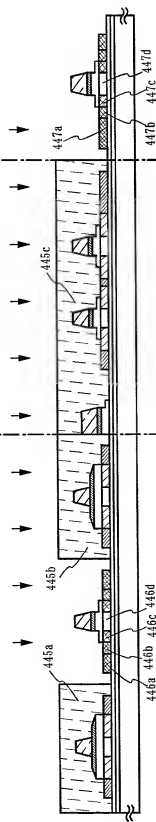


Fig. 6B

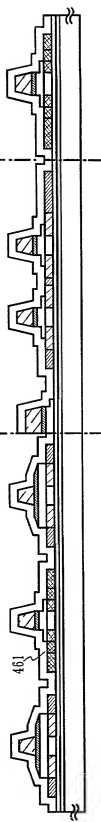


Fig. 6C

formation of the second interlayer insulating film / formation of pixel electrode and wirings

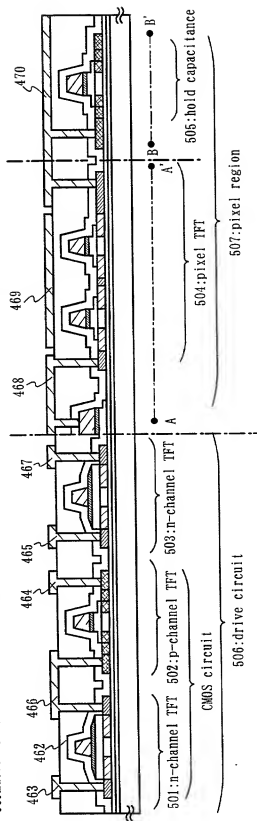


Fig. 7

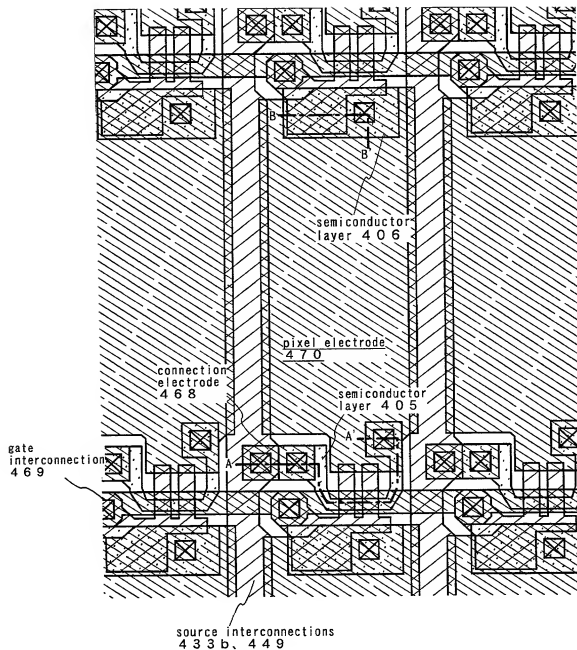


Fig. 8



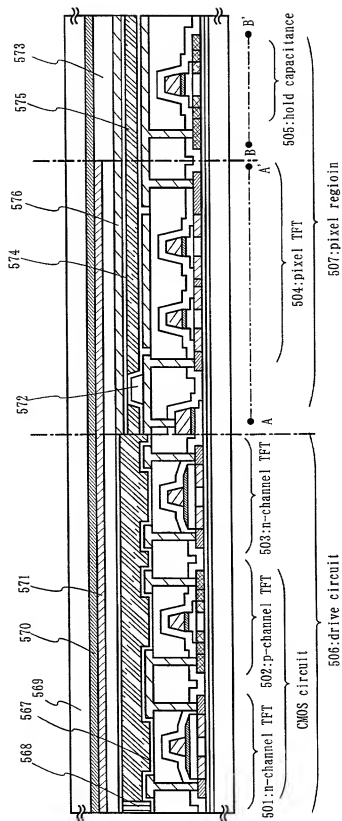


Fig. 9

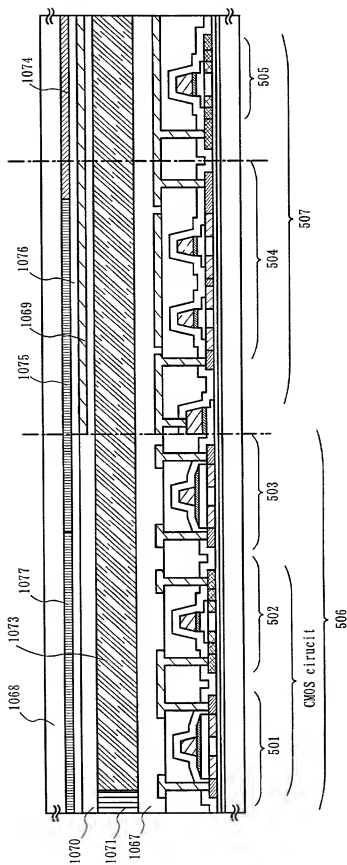


Fig. 10

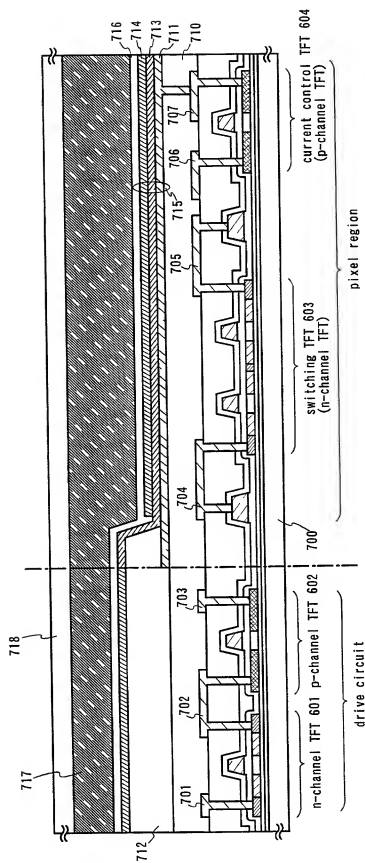


Fig. 11

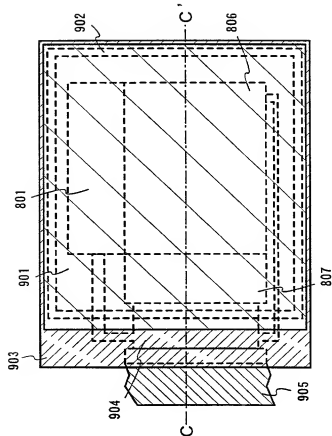


Fig. 12A

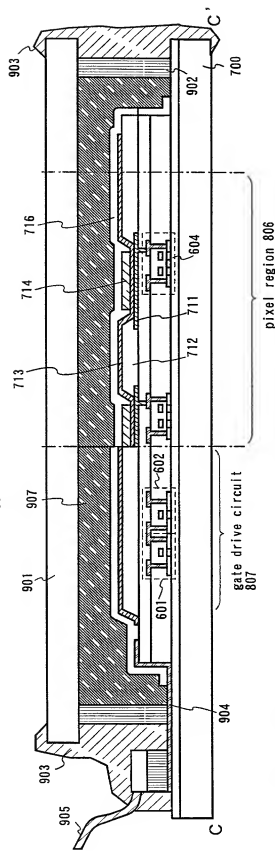


Fig. 12B

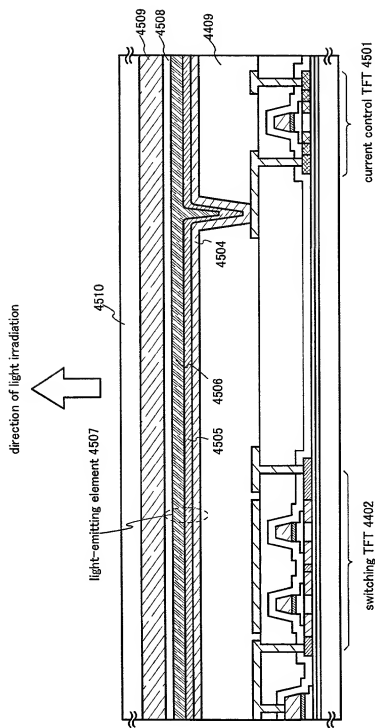


Fig. 13

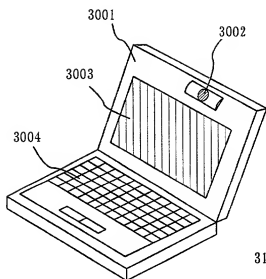


Fig. 14A

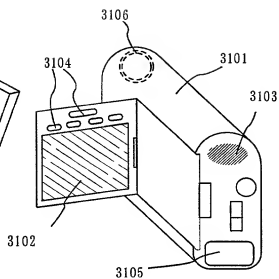


Fig. 14B

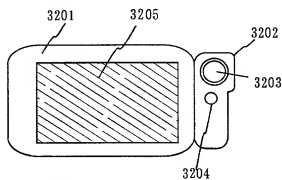


Fig. 14C

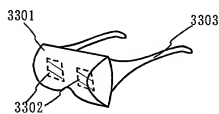


Fig. 14D

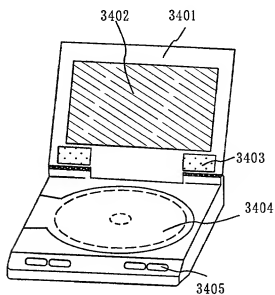


Fig. 14E

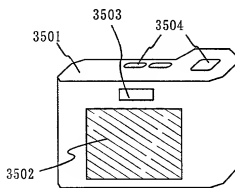


Fig. 14F

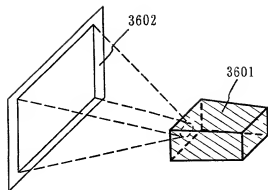


Fig. 15A

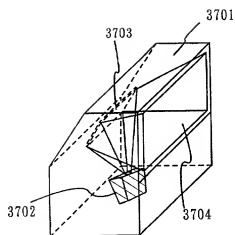


Fig. 15B

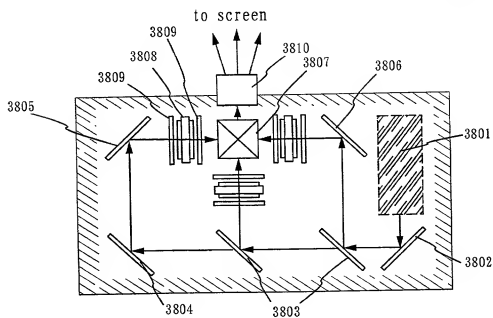


Fig. 15C

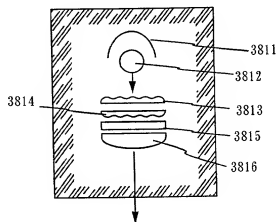


Fig. 15D

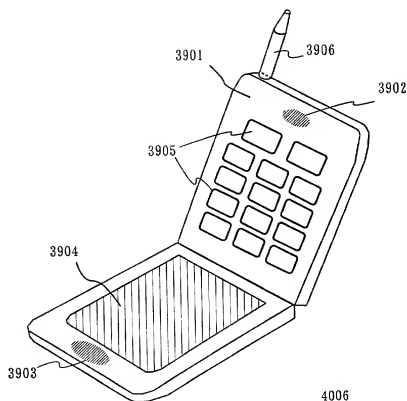


Fig. 16A

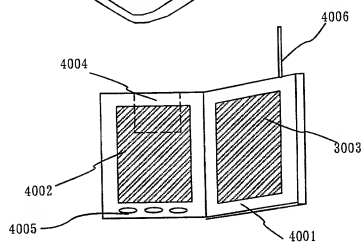


Fig. 16B

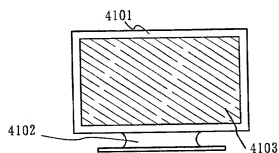


Fig. 16C



Fig. 17A

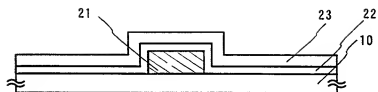


Fig. 17B

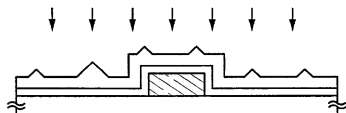


Fig. 17C

